

Note : Connector更換為USB Dongle, DIP插貼的

PT2.2=Low, Normal Mode  
PT2.2=High, Bootloader Mode

The diagram illustrates a 5V to 3.3V buck converter circuit. The input voltage is  $V_{IN} = U_{VBUS} = 5V$  and the output voltage is  $V_{OUT} = 3.3V$ . The circuit components and their connections are as follows:

- Input Filter:** A 10 $\mu F$  capacitor (C1) is connected between the input and ground (VSS).
- Regulator:** The AMS1117-3.3 (U3) is a three-terminal voltage regulator. Its VIN pin is connected to the input, its GND pin is connected to ground, and its VOUT pin is connected to the output.
- Output Filter:** A 10 $\mu F$  capacitor (C2) is connected between the output and ground (VSS).
- Load:** A 270 $\Omega$  resistor (R10) is connected between the output and ground (VSS).
- LED:** A blue LED (LD1) is connected between the output and ground (VSS).
- Diode:** A Schottky diode (D1, 5819) is connected in parallel with the load, with its cathode to the output and its anode to ground.
- Current Limiting:** A 0.5A fuse (F1) is connected in series with the input.

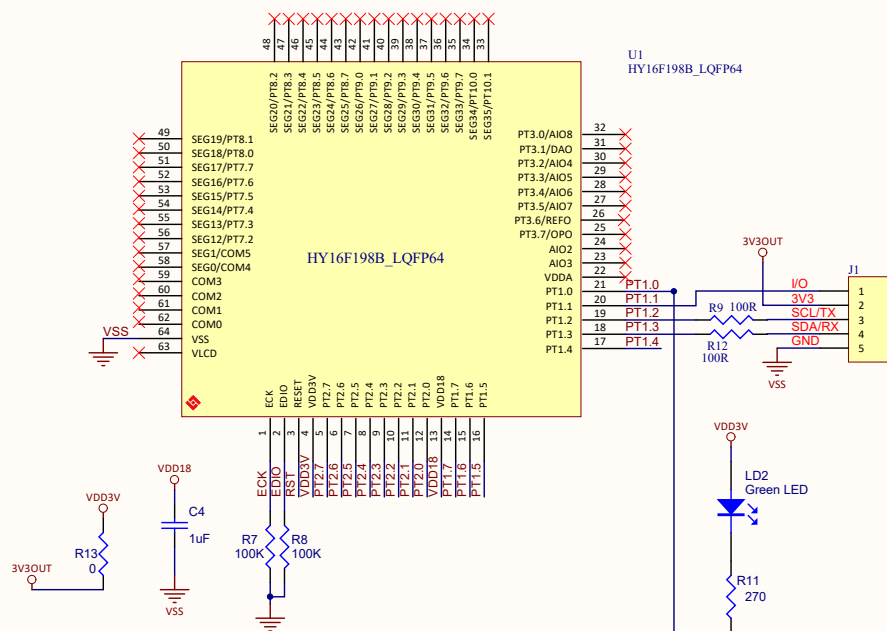
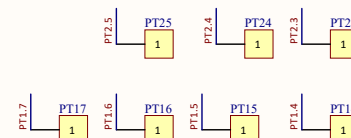


Diagram illustrating the EDM package pin connections:

- Pin 1: RST
- Pin 2: VDD3V
- Pin 3: ECK
- Pin 4: EDIO
- Pin 5: VSS

The package is labeled J3 at the top and EDM at the bottom.



剩餘空Pin留焊點在背面找空間塞即可, 不上件

Title <b>HY10000-CM01 : HY-Dongle Board</b>		
Size B	Number <b>A19023</b>	Revision <b>V02</b>
Date: 2020/1/21	Sheet of	
File: C:\Users\A19023\Documents\HY10000-CM01.schdoc	Drawn By:	